

OLED

96X36

0.67"



■ PHYSICAL DATA

No.	Items:	Specification:	Unit
1	Diagonal Size	0.67	Inch
2	Resolution	96(H) x 36(V)	Lines
3	Active Area	16.11(W) x 5.67(H)	mm
4	Outline Dimension (Panel)	23.13(W) x 15.67(H)	mm
5	Pixel Pitch	0.168(W) x 0.158(H)	mm
6	Pixel Size	0.148(W) x 0.138(H)	mm
7	Driver IC	SH1101	-
8	Grayscale	mono	-
9	Interface	Parallel / Serial	-
10	IC package type	COG	-
11	Thickness	1.6	mm
12	Weight	<1.1	g
13	Duty	1/36	-

■ SERIES PRODUCTS LIST

Module No.	Display Color	CIE		Luminance typical(cd/m ²)	Lifetime(hrs@25°C)	
		x	y		30% ON*	100% ON*
TOD9M0028-R-E	Red	0.65	0.34	30	150K	45K
				50	75K	22K
TOD9M0028-Y-E	Yellow	0.46	0.51	60	150K	45K
				100	70K	21K
TOD9M0028-G-E	Green	0.31	0.62	80	80K	24K
				100	55K	16K
TOD9M0028-B-E	Blue	0.16	0.27	60	80K	24K
				80	60K	18K
TOD9M0028-W-E	White	0.3	0.36	60	80K	24K
				80	60K	18K

* 30% ON: 30% pixels scrolling display on; 100% ON: All pixels display on

EXTERNAL DIMENSIONS

Customer No.:

29	VPP
28	VCDMH
27	IREF
26	D7
25	D6
24	D5
23	D4
22	D3
21	D2
20	D1
19	DD
18	/RD
17	/WR
16	A0
15	/RES
14	/CS
13	NC
12	P/S
11	C86
10	VDD1
9	NC
8	NC
7	NC
6	VBREF
5	SENSE
4	FB
3	VDD2
2	SW
1	VSS

NOTES:
 1.符合ROHS环保标准
 2.FPC:TOD9M0028FPC-A1-E
 2.GENERAL TOLERANCE:±0.2

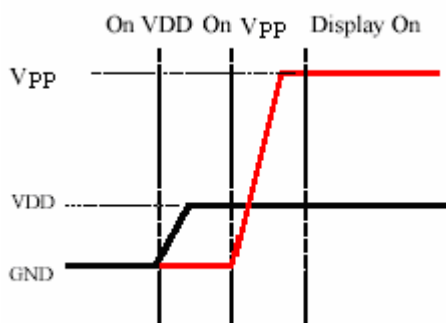
CUSTOMER APPROVE		NO.	NAME	QTY	Part No.	VENDOR & NOTE	
Mechanical	Electrical	AMEND			TRULY SEMICONDUCTORS LTD.		
				1/36	PRODUCT NO.	DRAW NO.	REV
					TOD9M0028	OLED089	C
			更正右视图FPC双面粘朝向	20070815	Vop	DWN	黎洪由20070815
			更正IC面朝向/逻辑走线	20070507	VDD	CHKD	谢志生20070815
					0'clock		APPD 苏君海20070815
			NO.	CONTENT	DATE	0'clock	NDT IN SCALE UNIT mm SHEET: 1/3

TIMING OF POWER SUPPLY

To Protect OLED panel and extend the panel life time, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources turn on/off.

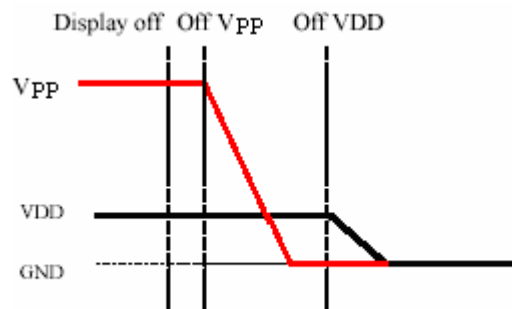
Power up Sequence:

1. Power up V_{DD}
2. Delay 100ms
3. Power up V_{PP} (High Voltage)
4. Delay 100ms
5. Send Display ON command



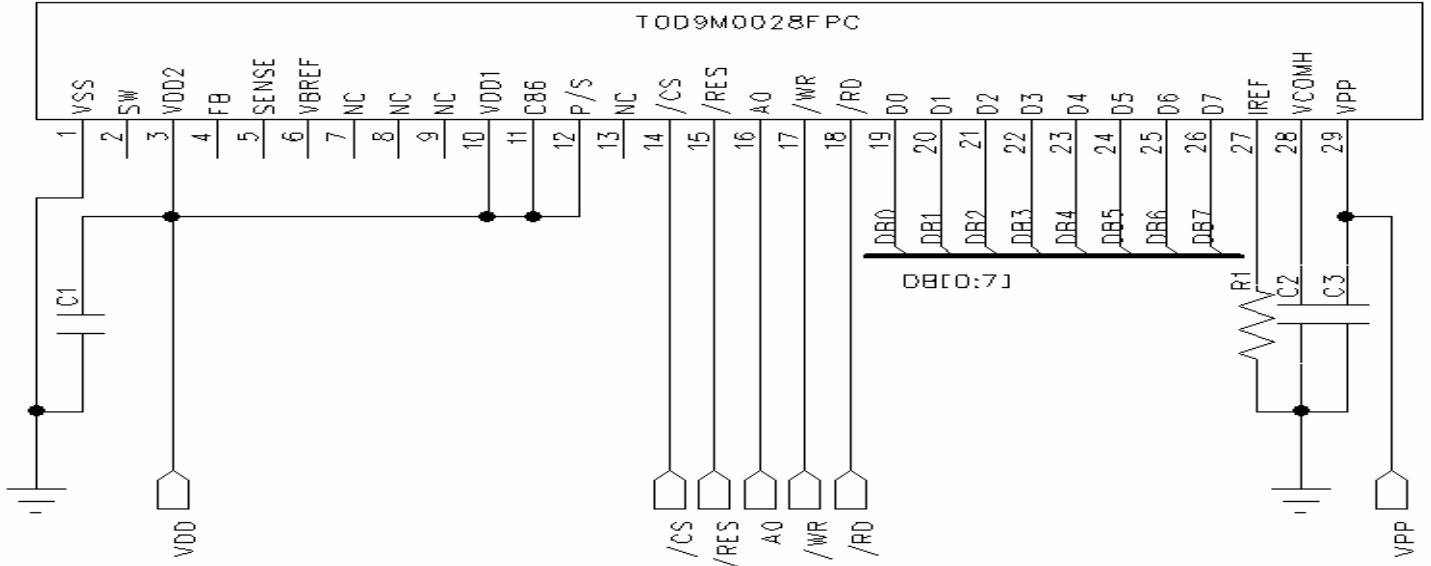
Power down Sequence:

1. Send Display OFF command
2. Power down V_{PP}(High Voltage)
3. Delay 100ms
4. Power down V_{DD}

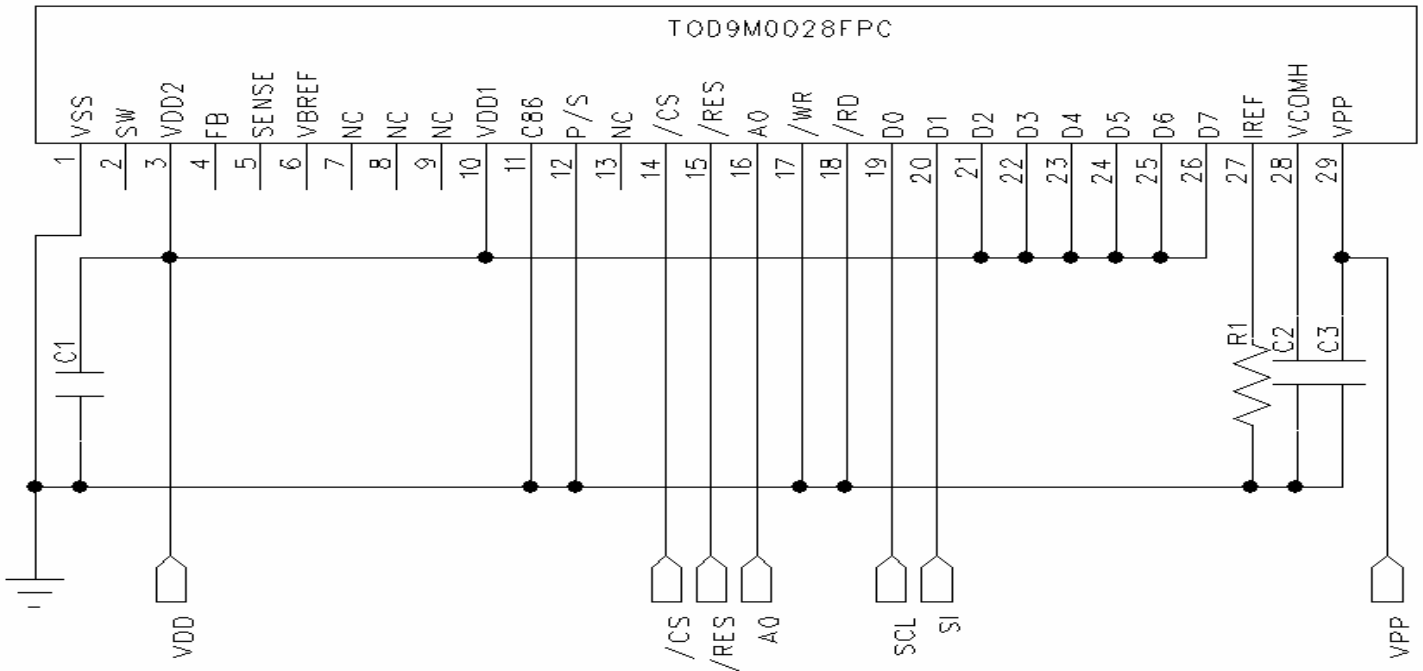


■ SCHEMATIC EXAMPLE

◆ 8080 Series Interface Application Circuit(External VPP):



◆ Serial Interface Application Circuit(External VPP):



NOTE:

1. R1=910KΩ,C1=C2=C3=4.7uF

2.In serial interface mode , the read function is not possible.

■ Contact Information:

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