

OLED
128X64
1.02"



■ **PHYSICAL DATA**

No.	Items:	Specification:	Unit
1	Diagonal Size	1.02	Inch
2	Resolution	128(H) x 64(V)	Lines
3	Active Area	23.02(W) x 11.86(H)	mm
4	Outline Dimension (Panel)	30.00(W) x 20.16(H)	mm
5	Pixel Pitch	0.180(W) x 0.180(H)	mm
6	Pixel Size	0.160(W) x 0.160(H)	mm
7	Driver IC	SH1101A	-
8	Grayscale	mono	-
9	Interface	Parallel / Serial	-
10	IC package type	COG	-
11	Thickness	1.6	mm
12	Weight	<2.0	g
13	Duty	1/64	-

■ **SERIES PRODUCTS LIST**

Module No.	Display Color	CIE		Luminance typical(cd/m ²)	Lifetime (hrs, 25°C)	
		x	y		30% ON*	100% ON*
TOD9M0041-A2-E	Blue+Yellow	0.16±0.05	0.27±0.05	60	80K	24K
				80	60K	18K
TOD9M0041-R-E	Red	0.65±0.04	0.34±0.04	30	150K	45K
				50	75K	22K
TOD9M0041-G-E	Green	0.31±0.04	0.62±0.04	80	80K	24K
				100	55K	16K
TOD9M0041-Y-E	Yellow	0.46±0.05	0.51±0.05	60	150K	45K
				100	70K	22K
TOD9M0041-W-E	White	0.3±0.05	0.36±0.05	60	80K	24K
				80	60K	18K

* 30% ON:30% pixels scrolling display on;100% ON:all pixels display on

1、 **Life Time** is defined when the Luminance has decayed to less than 50% of the initial Luminance

EXTERNAL DIMENSIONS

Customer No. :

PIN DESCRIPTION

29	VPP
28	VCOMH
27	IREF
26	D7
25	D6
24	D5
23	D4
22	D3
21	D2
20	D1
19	D0
18	/RD
17	/WR
16	A0
15	/RES
14	/CS
13	NC
12	P/S
11	C86
10	VDD1
9	NC
8	NC
7	NC
6	VBREF
5	SENSE
4	FB
3	VDD2
2	SW
1	VSS

detail:"A"/scale 10X

NOTES:

- GENERAL TOLERANCE: ± 0.2
- OPERATING TEMPERATURE: -20°C TO 70°C
- STORAGE TEMPERATURE: -30°C TO 80°C
- DRIVE IC : SH1101
- FPC: TOD9M0028FPC-A1-E
- ROHS COMPLIANCY

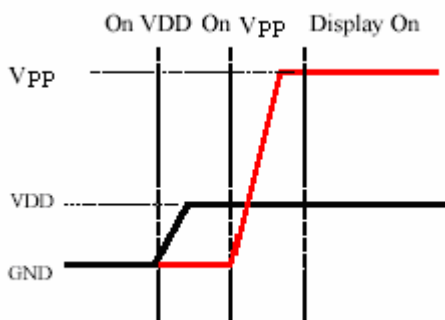
CUSTOMER APPROVE		AMEND		NO.	NAME	QTY	Part No.
Mechanical	Electrical			OLED	TRULY SEMICONDUCTORS LTD.		
				1/64	DUTY		PRODUCT NO.
					REAS		TOD9M0041
					Yop		DRN 黎洪由20071022
					Yop		CHKD 谢志生20071022
					Yop		APPD 苏君海20071022
					Yop		NOT IN SCALE
					Yop		UNIT mm
					Yop		SHEET: 3/3

TIMING OF POWER SUPPLY

To Protect OLED panel and extend the panel life time, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources turn on/off.

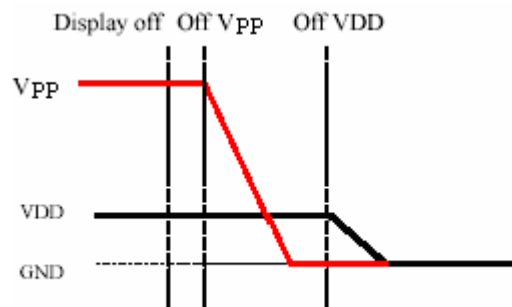
Power up Sequence:

1. Power up V_{DD}
2. Delay 100ms
3. Power up V_{PP} (High Voltage)
4. Delay 100ms
5. Send Display ON command



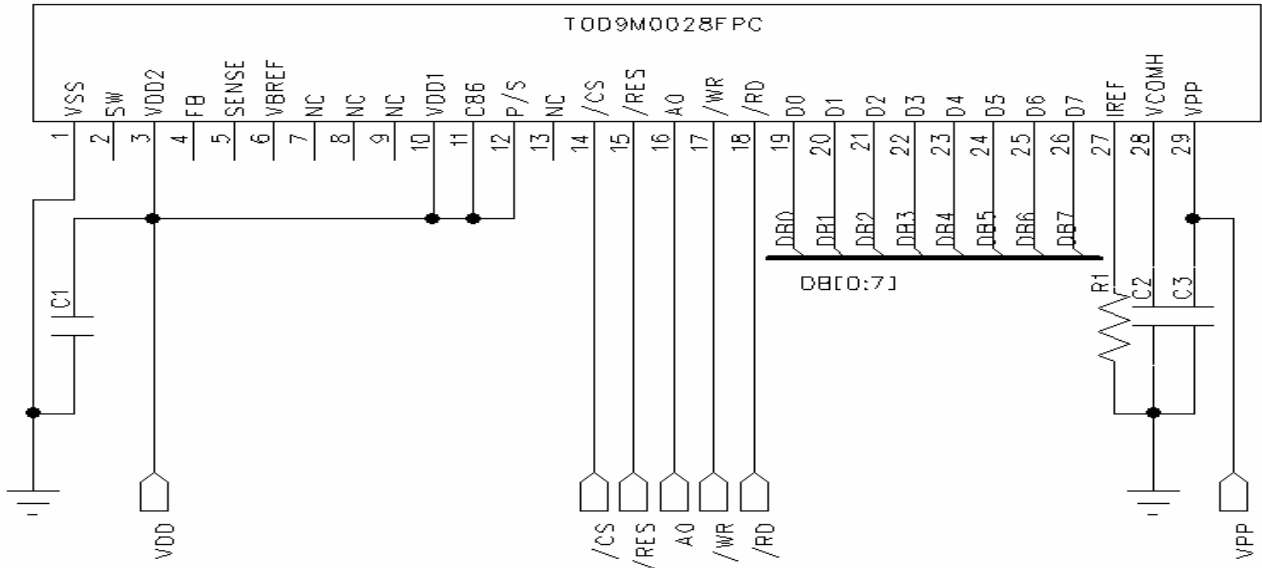
Power down Sequence:

1. Send Display OFF command
2. Power down V_{PP} (High Voltage)
3. Delay 100ms
4. Power down V_{DD}

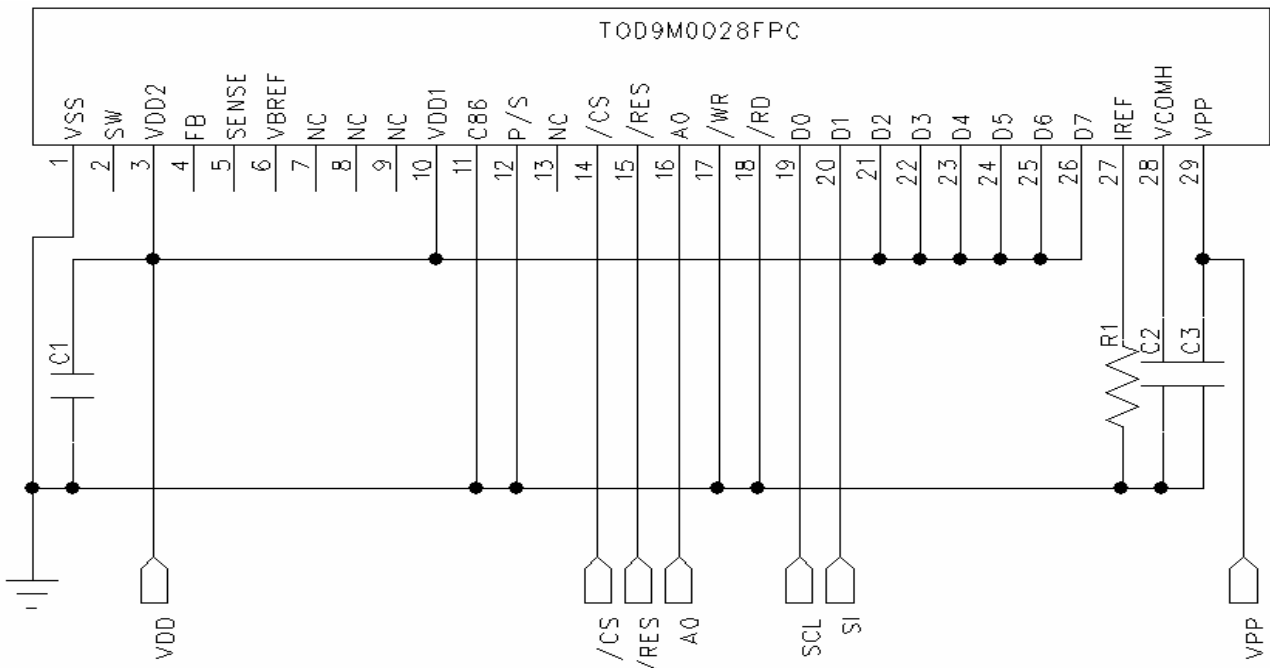


■ SCHEMATIC EXAMPLE

◆ 8080 Series Interface Application Circuit(External VPP):



◆ Serial Interface Application Circuit(External VPP):



NOTE:

1. R1=910KΩ,C1=C2=C3=4.7uF

2.In Serial interface mode , read function is not possible.

■ Contact Information:

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