

**PRODUCT** : LCD MODULE

**MODEL NO.** : TFT1280120-1-E

**SUPPLIER** : TRULY SEMICONDUCTORS LTD.

**DATE** : March 5, 2010



CERT. No. QAC0946535  
(ISO9001)



CERT. No. HKG002005  
(ISO14001)

# SPECIFICATION

Revision: 1.1

**TFT1280120-1-E**

This module uses ROHS material

This specification maybe changed without any notice in order to improve performance or quality etc. Please contact TRULY R&D department for update specification and product status before design for this product or release the order.

**TRULY SEMICONDUCTORS LTD:**

**CUSTOMER:**

Quality Assurance Department: \_\_\_\_\_

Approved by:

Technical Department: \_\_\_\_\_

Approved by:



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HUANG SU LING	FAN JIE	ZHU JIN BAO

**■ GENERAL INFORMATION**

<b>Item</b>	<b>Contents</b>	<b>Unit/Note</b>
LCD type	TFT	/
Viewing direction	12:00 O' Clock	6:00O' Clock(good viewing)
Module area (W × H)	288.92 × 38.6	mm <sup>2</sup>
Viewing area (W×H)	282.32 × 29.0	mm <sup>2</sup>
Active area (W×H)	280.32 × 27.0	mm <sup>2</sup>
Number of Dots	1280 (RGB) × 120	/
Pixel pitch (W × H)	0.219 × 0.225	mm <sup>2</sup>
Driver IC	HX8655-C & HX8232-B	/
Backlight Type	LED	/
Module Power consumption	3444	mw
Interface Type	Digital RGB 18bit interface	/
Input voltage	3.3	V



## ■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	$V_{DD}$	-0.3	3.6	V
Input voltage	$V_{IN}$	-0.3	$V_{DD} + 0.3$	V
Operating temperature	$T_{OP}$	-20	70	°C
Storage temperature	$T_{ST}$	-30	80	°C
Humidity	RH	-	90%(Max60°C)	RH

## ■ ELECTRICAL CHARACTERISTICS

### DC CHARACTERISTICS

Parameter	Symbol	Min	Typ	Max	Unit
Supply voltage for logic	$V_{DD}-V_{SS}$	3.2	3.3	3.4	V
Input Current	$I_{dd}$	-	260	520	mA
Input voltage 'H' level	$V_{IH}$	$0.8V_{DD}$	-	$V_{DD}$	V
Input voltage 'L' level	$V_{IL}$	0	-	$0.2V_{DD}$	V
Output voltage 'H' level	$V_{OH}$	$0.8V_{DD}$	-	$V_{DD}$	V
Output voltage 'L' level	$V_{OL}$	0	-	$0.2V_{DD}$	V

## ■ TIMING OF POWER SUPPLY

PLEASE REFER TO THE DRIVER IC SPECIFICATION.

## ■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage	$V_f$	9.0	9.6	10.5	V	If=180mA, Ta=25°C
Luminance	$L_v$	-	6000	-	Cd/m <sup>2</sup>	
Number of LED	-	27			Piece	-
Connection mode	Series/parallel	3Series,9parallel			-	-

Using condition: constant current driving method If= 180 mA (+/-10%)

**■ELECTRO-OPTICAL CHARACTERISTICS**

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark	Note
Response time	Tr +Tf	θ=0° ∅=0° Ta=25°C	—	27	41	ms	Fig.1	4
Contrast ratio	Cr		300	776	—	---	FIG 2.	1
Luminance uniformity	δ WHITE		70	78	—	%	FIG 2.	3
Surface Luminance	Lv		406	507	—	cd/m <sup>2</sup>	FIG 2.	2
Viewing angle range	θ	∅ = 90°	70	80	—	deg	FIG 3.	6
		∅ = 270°	70	80	—	deg	FIG 3.	
		∅ = 0°	70	80	—	deg	FIG 3.	
		∅ = 180°	70	80	—	deg	FIG 3.	
CIE (x, y) chromaticity	Red x	θ=0° ∅=0° Ta=25°C	0.5319	0.5819	0.6319	-	FIG 2.	5
	Red y		0.2798	0.3298	0.3798			
	Green x		0.3007	0.3507	0.4007			
	Green y		0.5142	0.5642	0.6142			
	Blue x		0.0971	0.1471	0.1971			
	Blue y		0.0285	0.0785	0.1285			
	White x		0.2506	0.3106	0.3706			
	White y		0.2630	0.3230	0.3830			

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.:

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P}_1, P_2, P_3, P_4, P_5)}{\text{Average Surface Luminance with all black pixels (P}_1, P_2, P_3, P_4, P_5)}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P}_1, P_2, P_3, P_4, P_5)$$

Note 3. The uniformity in surface luminance, δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P}_1, P_2, P_3, P_4, P_5)}{\text{Maximum Surface Luminance with all white pixels (P}_1, P_2, P_3, P_4, P_5)}$$

Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series

Note 5. CIE (x, y) chromaticity, The x,y value is determined by measuring luminance at each test position 1 through 5, and then make average value

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

Note 8. For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

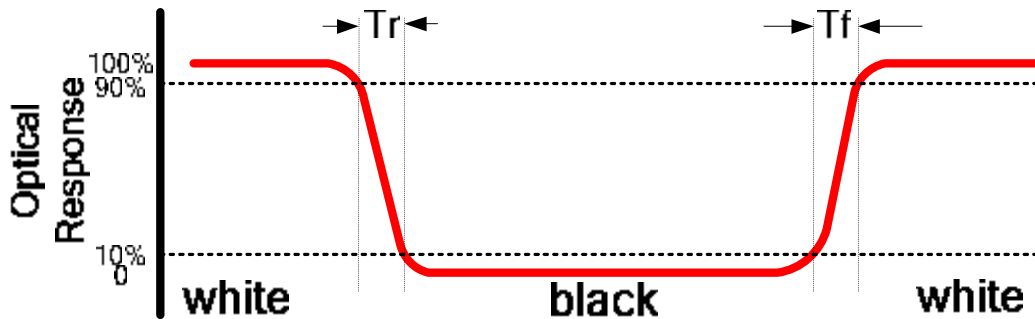


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm  
 B : 5 mm  
 H, V : Active Area  
 Light spot size  $\varnothing=5\text{mm}$ , 500mm distance from the LCD surface to detector lens  
 measurement instrument is TOPCON's luminance meter BM-5

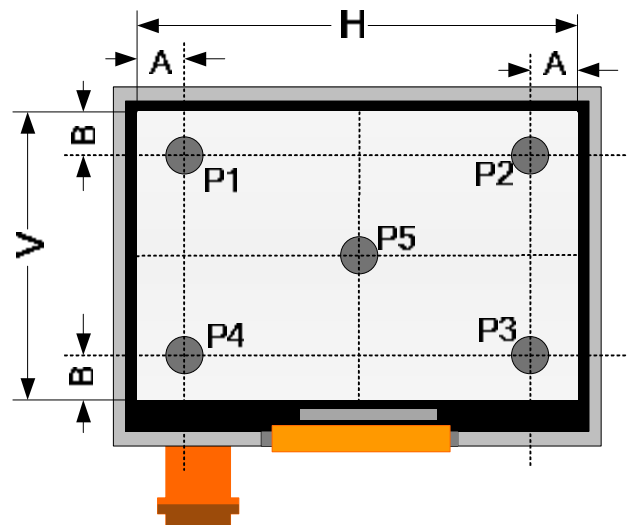
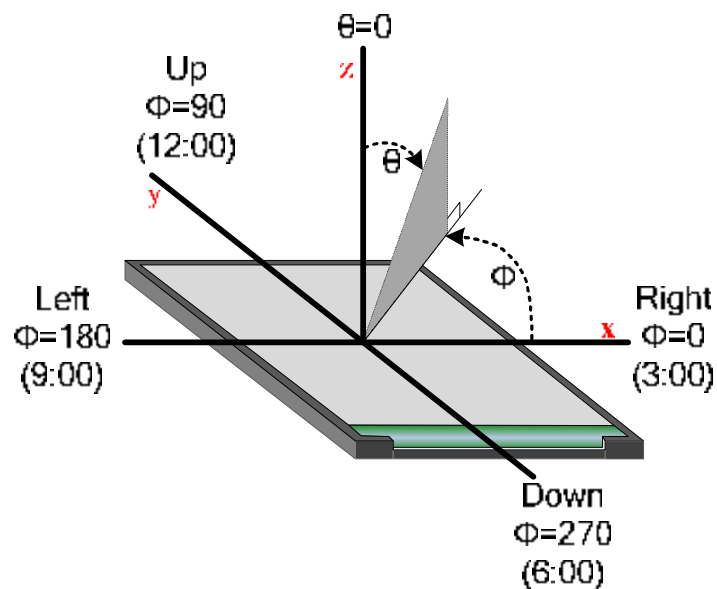


FIG.3. The definition of viewing angle



**■ INTERFACE DESCRIPTION**

Pin No.	Symbol	Level	Description
1	VDD	3.3V	Digital power supply
2	VDD	3.3V	Digital power supply
3~8	B0~B5	H/L	Blue Data
9	VSS	0V	Ground
10~15	G0~G5	H/L	Green Data
16	VSS	0V	Ground
17~22	R0~R5	H/L	Red Data
23	VSS	0V	Ground
24	DCLK	H/L	Data Clock
25	VSS	0V	Ground
26	HSYNC	H/L	Horizontal Synchronous Signal
27	VSNC	H/L	Vertical Synchronous Signal
28	DEN	H/L	Data enabling signal
29	LEDK	---	LED light cathode
30	LEDA	---	LED light anode LEDA-LEDK=9.6V(TYP)

**■ APPLICATION CIRCUIT**

Please consult our technical department for detail information.

■ RELIABILITY

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80±2°C/200 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	-30±2°C/200 hours	
3	High Temperature Operating	70±2°C/120 hours	
4	Low Temperature Operating	-20±2°C/120 hours	
5	Temperature Cycle	-20±2°C~25~70±2°C×10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test	50°C±5°C×90%RH/120 hours	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours (Packing condition)	
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance>10MΩ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.  
Using ionizer(an antistatic blower) is recommended at working area in order to reduce electro-static voltage.  
When removing protection film from LCM panel, peel off the tag slowly( recommended more than one second) while blowing with ionizer toward the peeling face to minimize ESD which may damage electrical circuit.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6.Please use automatic switch menu(or roll menu) testing mode when test operating mode.

## ■ INSPECTION CRITERIA

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product.

### 1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

Minor defect: AQL 1.5

### 2. Inspection condition

● Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

● Driving voltage

The Vop value from which the most optimal contrast can be obtained near the specified Vop in the specification (Within ±0.5V of the typical value at 25°C.).

### 3. Definition of inspection zone in LCD.

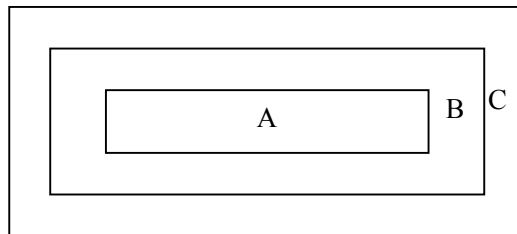


Fig.4

Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.4 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

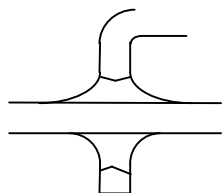
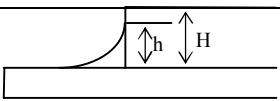
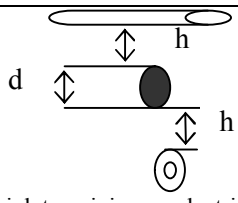
## 4. Inspection Standard

### 4.1 Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting.	Major
4.1.2	Missing	Missing component	
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	

### 4.2 Cosmetic Defect

#### 4.2.1 Module Cosmetic Criteria

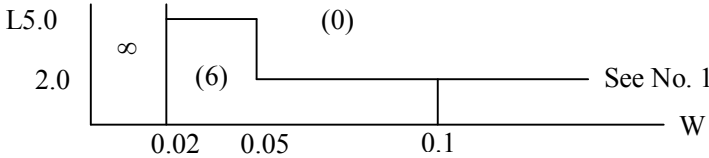
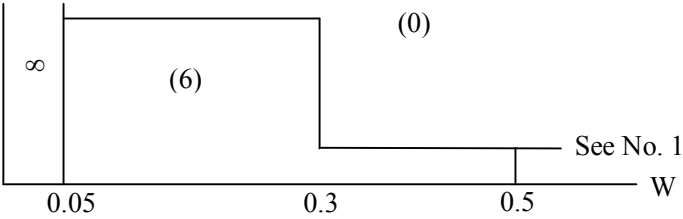
No.	Item	Judgement Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor
4	Resist flaw on Printed Circuit Boards	visible copper foil ( $\varnothing 0.5\text{mm}$ or more) on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed $\varnothing 0.2\text{mm}$ )	Minor Minor
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor
8	Solder amount	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	1. Lead parts		
	2. Flat packages	Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder.	Minor
	3. Chips	$(3/2) H \geq h \geq (1/2) H$ 	Minor
9	Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13\text{mm}$ The diameter of solder ball $d \leq 0.15\text{mm}$ . b. The quantity of solder balls or solder Splashes isn't beyond 5 in $600\text{mm}^2$ . c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Minor Minor Major
			

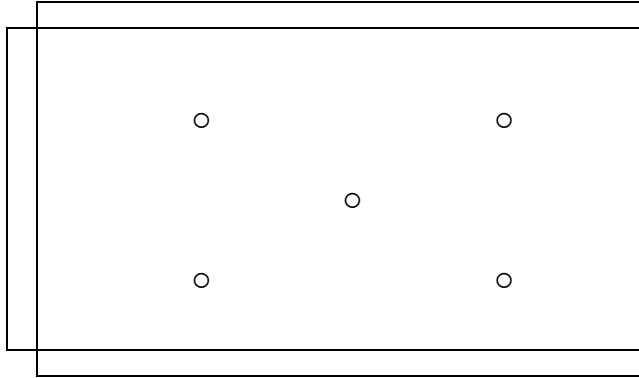
		<p>d. Solder balls/Solder splashes must be entrapped/encapsulated Or attached to the metal surface .</p> <p>NOTE: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.</p>	<p>Minor</p>
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**4.2.2Cosmetic Criteria (Non-Operating)**

No.	Defect	Judgment Criterion	Partition										
1	Spots	In accordance with <i>Screen Cosmetic Criteria (Operating) No.1.</i>	Minor										
2	Lines	In accordance with <i>Screen Cosmetic Criteria (Operating) No.2.</i>	Minor										
3	Bubbles in polarizer	<table border="1" data-bbox="499 672 1275 853"> <thead> <tr> <th data-bbox="499 672 804 705">Size : d mm</th> <th data-bbox="804 672 1275 705">Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td data-bbox="499 705 804 739">d ≤ 0.3</td> <td data-bbox="804 705 1275 739">Disregard</td> </tr> <tr> <td data-bbox="499 739 804 772">0.3 &lt; d ≤ 1.0</td> <td data-bbox="804 739 1275 772">3</td> </tr> <tr> <td data-bbox="499 772 804 806">1.0 &lt; d ≤ 1.5</td> <td data-bbox="804 772 1275 806">1</td> </tr> <tr> <td data-bbox="499 806 804 840">1.5 &lt; d</td> <td data-bbox="804 806 1275 840">0</td> </tr> </tbody> </table>	Size : d mm	Acceptable Qty in active area	d ≤ 0.3	Disregard	0.3 < d ≤ 1.0	3	1.0 < d ≤ 1.5	1	1.5 < d	0	Minor
Size : d mm	Acceptable Qty in active area												
d ≤ 0.3	Disregard												
0.3 < d ≤ 1.0	3												
1.0 < d ≤ 1.5	1												
1.5 < d	0												
4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor										
5	Allowable density	Above defects should be separated more than 30mm each other.	Minor										
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor										
7	Contamination	Not to be noticeable.	Minor										

4. 2. 3 Cosmetic Criteria (Operating)

No.	Defect	Judgment Criterion	Partition																																												
1	Spots	<p>A) Clear</p> <table border="1" data-bbox="443 376 1278 719"> <thead> <tr> <th>Lcd size</th> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Lcd size ≤ 8.0'</td> <td>d ≤ 0.1</td> <td>Disregard</td> </tr> <tr> <td>0.1 &lt; d ≤ 0.2</td> <td>6</td> </tr> <tr> <td>0.2 &lt; d ≤ 0.3</td> <td>2</td> </tr> <tr> <td>0.3 &lt; d</td> <td>0</td> </tr> <tr> <td rowspan="4">Lcd size &gt; 8.0'</td> <td>d ≤ 0.1</td> <td>Disregard</td> </tr> <tr> <td>0.1 &lt; d ≤ 0.3</td> <td>10</td> </tr> <tr> <td>0.3 &lt; d ≤ 0.5</td> <td>5</td> </tr> <tr> <td>0.5 &lt; d</td> <td>0</td> </tr> </tbody> </table> <p>Note : Including pin holes and defective dots which must be within one pixel size; Total defective point shall not exceed 6 pcs no more than 8 inch LCD and 10PCS for more than 8 inch LCD.</p> <p>B) Unclear</p> <table border="1" data-bbox="454 925 1289 1310"> <thead> <tr> <th>Lcd size</th> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Lcd size ≤ 8.0'</td> <td>d ≤ 0.2</td> <td>Disregard</td> </tr> <tr> <td>0.2 &lt; d ≤ 0.5</td> <td>6</td> </tr> <tr> <td>0.5 &lt; d ≤ 0.7</td> <td>2</td> </tr> <tr> <td>0.7 &lt; d</td> <td>0</td> </tr> <tr> <td rowspan="5">Lcd size &gt; 8.0'</td> <td>d ≤ 0.2</td> <td>Disregard</td> </tr> <tr> <td>0.2 &lt; d ≤ 0.5</td> <td>10</td> </tr> <tr> <td>0.5 &lt; d ≤ 0.7</td> <td>3</td> </tr> <tr> <td>0.7 &lt; d ≤ 1.0</td> <td>1</td> </tr> <tr> <td>1.0 &lt; d</td> <td>0</td> </tr> </tbody> </table> <p>Note : Total defective point shall not exceed 6 pcs for no more than 8 inch LCD and 10PCS for more than 8 inch LCD.</p>	Lcd size	Size : d mm	Acceptable Qty in active area	Lcd size ≤ 8.0'	d ≤ 0.1	Disregard	0.1 < d ≤ 0.2	6	0.2 < d ≤ 0.3	2	0.3 < d	0	Lcd size > 8.0'	d ≤ 0.1	Disregard	0.1 < d ≤ 0.3	10	0.3 < d ≤ 0.5	5	0.5 < d	0	Lcd size	Size : d mm	Acceptable Qty in active area	Lcd size ≤ 8.0'	d ≤ 0.2	Disregard	0.2 < d ≤ 0.5	6	0.5 < d ≤ 0.7	2	0.7 < d	0	Lcd size > 8.0'	d ≤ 0.2	Disregard	0.2 < d ≤ 0.5	10	0.5 < d ≤ 0.7	3	0.7 < d ≤ 1.0	1	1.0 < d	0	Minor
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2	Lines	<p>A) Clear</p>  <p>Note : ( ) - Acceptable Qty in active area  L - Length (mm)  W - Width (mm)  ∞ - Disregard</p> <p>B) Unclear</p>  <p>‘Clear’ = The shade and size are not changed by V<sub>op</sub>.  ‘Unclear’ = The shade and size are changed by V<sub>op</sub>.</p>	Minor																																												

3	Rubbing line	Not to be noticeable.	Minor
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'Spot'. (see <i>Screen Cosmetic Criteria (Operating) No.1</i> )	Minor
7	Uneven brightness (only back-lit type module)	<p>Uneven brightness must be <math>B_{MAX} / B_{MIN} \leq 2</math></p> <ul style="list-style-type: none"> <li>- <math>B_{MAX}</math> : Max. value by measure in 5 points</li> <li>- <math>B_{MIN}</math> : Min. value by measure in 5 points</li> </ul> <p>Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.</p>  <p>○ : Measuring points</p>	Minor

Note :

- (1) Size :  $d = (\text{long length} + \text{short length}) / 2$
- (2) The limit samples for each item have priority.
- (3) Complex defects are defined item by item, but if the numbers of defects are defined in above table, the total number should not exceed 10.
- (4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed. Following three situations should be treated as 'concentration'.
  - 7 or over defects in circle of  $\varnothing 5\text{mm}$ .
  - 10 or over defects in circle of  $\varnothing 10\text{mm}$ .
  - 20 or over defects in circle of  $\varnothing 20\text{mm}$ .

**Handling precaution for LCM**

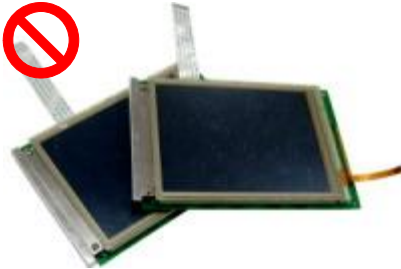
LCM is easy to be damaged. Please note below and be careful for handling.

**Correct handling:****Incorrect handling:**

As above picture, please handle with anti-static gloves around LCM edges.



Please don't touch IC directly.



Please don't stack LCM.



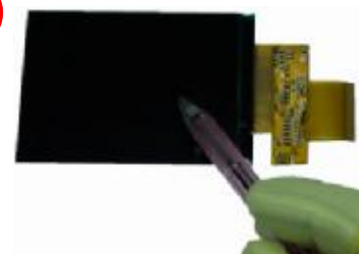
Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



Please don't hold the surface of IC.



Please don't operate with sharp stick such as pens.

**Storage Precautions**

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped.

**Others**

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

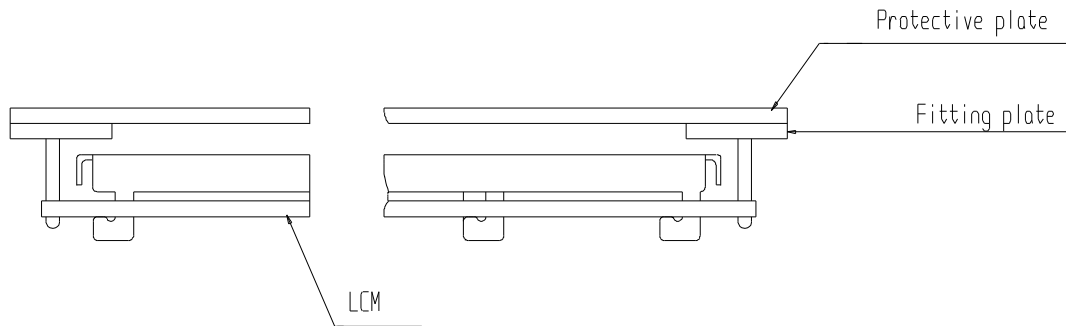
- Exposed area of the printed circuit board.
- Terminal electrode sections.

**■ USING LCD MODULES**

**Installing LCD Modules**

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

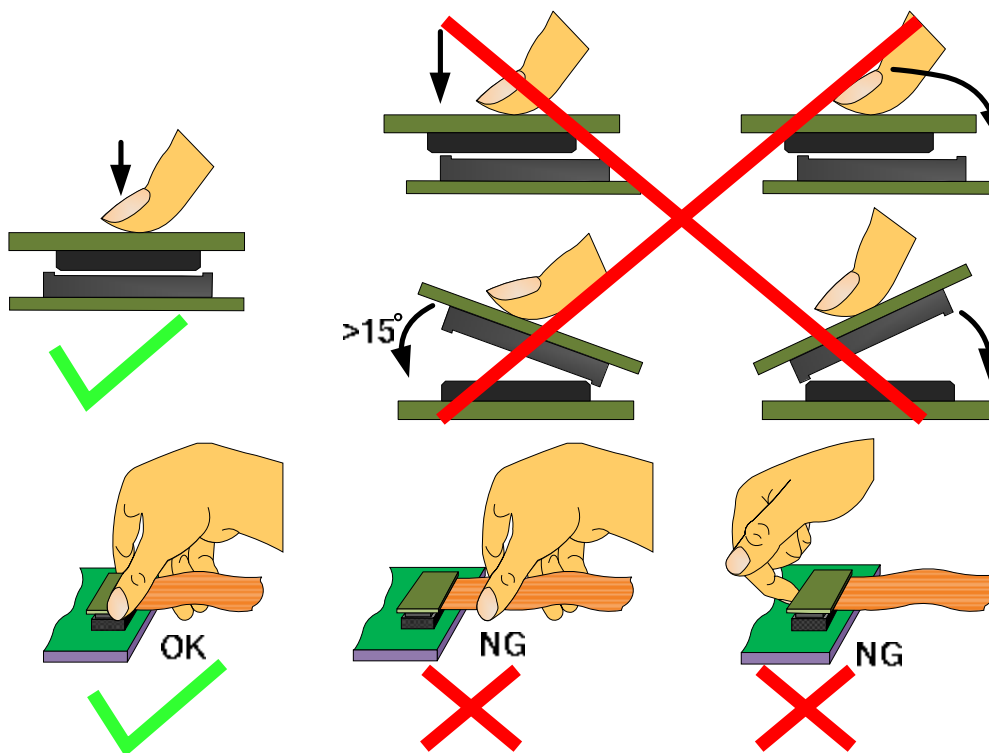
- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.

**Precaution for assemble the module with BTB connector:**

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



**Precaution for soldering the LCM**

	<b>Manual soldering</b>	<b>Machine drag soldering</b>	<b>Machine press soldering</b>
<b>No RoHS product</b>	290°C ~350°C. Time : 3-5S.	330°C ~350°C. Speed : 4-8 mm/s.	300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa
<b>RoHS product</b>	340°C ~370°C. Time : 3-5S.	350°C ~370°C. Time : 4-8 mm/s.	330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover

during soldering to prevent any damage due to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### **Precautions for Operation**

(1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.

(2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.

(3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.

(4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.

(6) Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.

(7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

**Safety**

(1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

**Limited Warranty**

Unless agreed between TRULY and customer, TRULY will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with TRULY LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to TRULY within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of TRULY limited to repair and/or replacement on the terms set forth above. TRULY will not be responsible for any subsequent or consequential events.

**Return LCM under warranty**

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

**■ PRIOR CONSULT MATTER**

- 1.①For Truly standard products, we keep the right to change material, process ... for improving the product property without notice on our customer.
- ②For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.
- 2.If you have special requirement about reliability condition, please let us know before you start the test on our samples.

**■ FACTORY**

**FACTORY NAME:** TRULY SEMICONDUCTORS LTD.

**FACTORY ADDRESS:** Truly Industrial Area, Shan Wei City, GuangDong, China P.C: 516600

**URL:** <http://www.truly.com.hk>      <http://www.trulysemi.com>