

PRODUCT : LCD MODULE

MODEL NO. : TFT240320-260-E

SUPPLIER : TRULY SEMICONDUCTORS LTD.

DATE : May 10, 2010



CERT. No. QAC0946535
(ISO9001)



CERT. No. HKG002005
(ISO14001)

SPECIFICATION

Revision: 1.3

TFT240320-260-E

This module uses ROHS material

If there is no special request from the customer, TRULY SEMICONDUCTORS LTD. will not reserve the tooling of the product under the following conditions:

1. There is no response from the customer in two years after TRULY SEMICONDUCTORS LTD. submit the samples.
2. There is no order in two years after the latest mass production.

And correlated data (including quality records) will be reserved for one year more after tooling is discarded.

TRULY SEMICONDUCTORS LTD:

CUSTOMER:

Quality Assurance Department: _____

Approved by: _____

Technical Department: _____

Approved by:

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WRITTEN BY	CHECKED BY	APPROVED BY
HUANG YAN XUE	FAN JIE	ZHU JIN BAO

■ GENERAL INFORMATION

Item	Contents	Unit
LCD type	TFT/TRANSFLECTIVE	/
Recommended Viewing Direction	12:00	O'Clock
Module area (W × H ×T)	57.54×79.20×3.65	mm ³
Viewing area (W×H)	50.60×66.80	mm ²
Active area (W×H)	48.60×64.80	mm ²
Number of Dots	240(RGB)×320	/
Pixel pitch (W × H)	0.2025×0.2025	mm ²
Driver IC	S6D04H0	/
Interface Type	System parallel interface/RGB interface	/
Input voltage	2.8	V
Module Power consumption	351	mw
Backlight Type	LED	/

■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	VCI	-0.3	3.3	V
Input voltage	VIN	-0.3	VCI+0.5	V
Operating temperature	Top	-20	70	°C
Storage temperature	TST	-30	80	°C
Humidity	RH	-	90%(Max60 °C)	RH

■ ELECTRICAL CHARACTERISTICS**DC CHARACTERISTICS**

Parameter	Symbol	Min	Typ	Max	Unit
Supply voltage for logic	VCI	2.5	2.8	3.3	V
Low voltage power supply	VDD3	1.65	2.8	3.3	V
Input Current	I _{dd}	-	19.81	39.62	mA
Input voltage 'H' level	V _{IH}	0.7VCI	-	VCI	V
Input voltage 'L' level	V _{IL}	0	-	0.3VCI	V
Output voltage 'H' level	V _{OH}	0.8VCI	-	VCI	V
Output voltage 'L' level	V _{OL}	0	-	0.2VCI	V

■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage	V _f	15	16	17	V	I _f =18mA
Luminance	L _v	2500	2900	3300	cd/m ²	T _a =25 °C
Number of LED	-	-	5	-	Piece	-
Connection mode	S	-	Series	-	-	-

Using condition: constant current driving method I_f=18mA(+/-10%).

■ ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark	Note
Response time	Tr+ Tf	θ=0° ∅=0° Ta=25°C	-	32	48	ms	Fig.1	4
Contrast ratio	Cr		52	103	-	---	FIG 2.	1
Luminance uniformity	δ WHITE		81	90	-	%	FIG 2.	3
Surface Luminance	Lv		90	112	-	cd/m ²	FIG 2.	2
Reflectivity	Re	-	-	3.7840	-	%	-	-
Viewing angle range	θ	∅ = 90°	70	80	-	deg	FIG 3.	6
		∅ = 270°	36	46	-	deg	FIG 3.	
		∅ = 0°	25	35	-	deg	FIG 3.	
		∅ = 180°	35	45	-	deg	FIG 3.	
CIE (x, y) chromaticity	Red x	θ=0° ∅=0° Ta=25°C	0.5067	0.5567	0.6067	-	FIG 2.	5
	Red y		0.2964	0.3464	0.3964	-		
	Green x		0.2928	0.3428	0.3928	-		
	Green y		0.4941	0.5441	0.5941	-		
	Blue x		0.1060	0.1560	0.2060	-		
	Blue y		0.0557	0.1057	0.1557	-		
	White x		0.2334	0.2934	0.3534	-		
	White y		0.2444	0.3044	0.3644	-		

Note1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{ContrastRatio} = \frac{\text{AverageSurface Luminance with all white pixels (P 1,P2, P 3,P4, P5)}}{\text{Average SurfaceLuminance with all black pixels (P1, P2, P 3,P4, P5)}}$$

Note2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P 3,P4, P5)}$$

Note3. The uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P}_1, \text{P}_2, \text{P}_3, \text{P}_4, \text{P}_5)}{\text{Maximum Surface Luminance with allwhite pixels (P}_1, \text{P}_2, \text{P}_3, \text{P}_4, \text{P}_5)}$$

Note4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1..

Note5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5 photo detector.

Note8. For TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

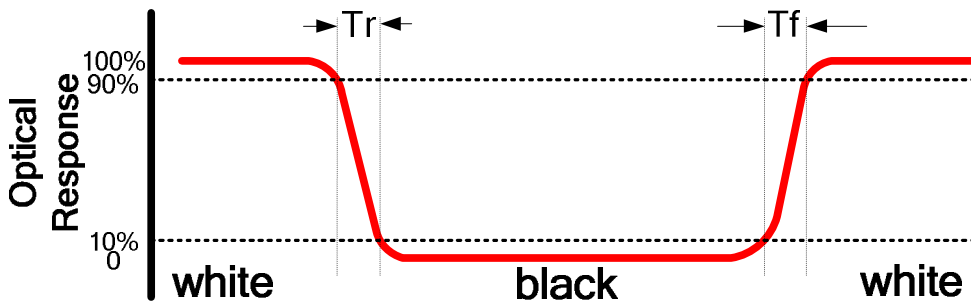


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm
 B : 5 mm
 H, V : Active Area
 Light spot size $\varnothing=5\text{mm}$, 500mm distance from the LCD surface to detector lens
 measurement instrument is TOPCON's luminance meter BM-5

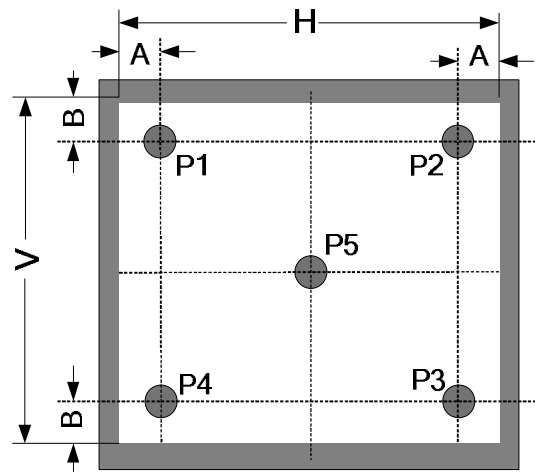
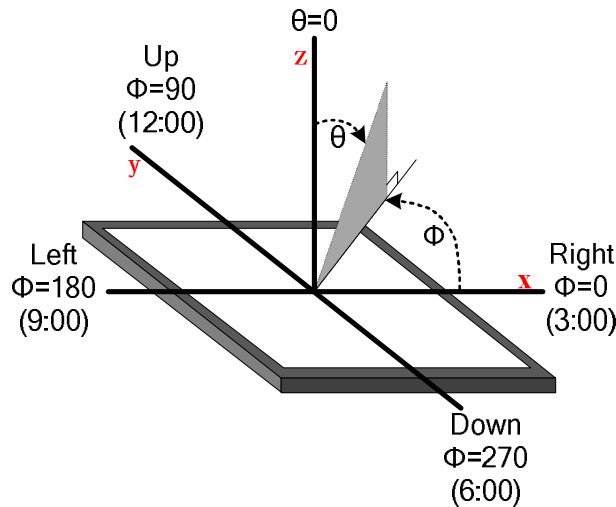


FIG.3. The definition of viewing angle



■ INTERFACE DESCRIPTION

No.	Symbol	I/O or connect to	Description	when not in use																																																																														
1	LED_A	LED driver	LED light cathode	OPEN																																																																														
2	LED_K	LED driver	LED light cathode	OPEN																																																																														
3	GND	I	POWER GROUND	-																																																																														
4	VCI	I	Power supply to liquid crystal power supply analog circuit.	-																																																																														
5~8	IM[3:0]	I	Selects the MPU interface mode	-																																																																														
			<table border="1"> <thead> <tr> <th>IM3</th> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>Interface mode</th> <th>Data pad</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>80 MCU 8-bit Parallel I/F</td> <td>DB[7:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>80 MCU 16-bit Parallel I/F</td> <td>DB[15:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>80 MCU 9-bit Parallel I/F</td> <td>DB[8:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>1</td> <td>80 MCU 18-bit Parallel I/F</td> <td>DB[17:0]</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>3-wire 9-bit data Serial interface I</td> <td>SDA : In/Out</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>4-wire 8-bit data Serial interface I</td> <td>SDA : In/Out</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>68 MCU 8-bit Parallel I/F</td> <td>DB[7:0]</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>1</td> <td>68 MCU 16-bit Parallel I/F</td> <td>DB[15:0]</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>68 MCU 9-bit Parallel I/F</td> <td>DB[8:0]</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>68 MCU 18-bit Parallel I/F</td> <td>DB[17:0]</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>1</td> <td>3-wire 9-bit data Serial interface II</td> <td>SDI : In / SDO : Out</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>0</td> <td>4-wire 8-bit data Serial interface II</td> <td>SDI : In / SDO : Out</td> </tr> </tbody> </table>		IM3	IM2	IM1	IM0	Interface mode	Data pad	0	0	0	0	80 MCU 8-bit Parallel I/F	DB[7:0]	0	0	0	1	80 MCU 16-bit Parallel I/F	DB[15:0]	0	0	1	0	80 MCU 9-bit Parallel I/F	DB[8:0]	0	0	1	1	80 MCU 18-bit Parallel I/F	DB[17:0]	0	1	0	1	3-wire 9-bit data Serial interface I	SDA : In/Out	0	1	1	0	4-wire 8-bit data Serial interface I	SDA : In/Out	1	0	0	0	68 MCU 8-bit Parallel I/F	DB[7:0]	1	0	0	1	68 MCU 16-bit Parallel I/F	DB[15:0]	1	0	1	0	68 MCU 9-bit Parallel I/F	DB[8:0]	1	0	1	1	68 MCU 18-bit Parallel I/F	DB[17:0]	1	1	0	1	3-wire 9-bit data Serial interface II	SDI : In / SDO : Out	1	1	1	0	4-wire 8-bit data Serial interface II	SDI : In / SDO : Out
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MPU Parallel interface bus and serial interface select																																																																																		
9	FRAME	O	Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. When this pin is not activated, this pin is low.	OPEN																																																																														
10	RESET	I	This signal will reset the device and must be applied to properly initialize the chip. Signal is active low.	-																																																																														
11	VSYNC	I	Vertical sync. Signal in RGB I/F mode.	VDD3/ GND																																																																														
12	HSYNC	I	Horizontal sync. Signal in RGB I/F mode.	VDD3/ GND																																																																														
13	DOTCLK	I	Pixel clock signal in RGB I/F mode.	VDD3/ GND																																																																														
14	ENABLE	I	Data enable signal in RGB I/F mode.	VDD3/ GND																																																																														
15~32	DB[17:0]	I/O	When RGB I/F, DB[17:0] are used to RGB interface data bus. When MPU I/F, DB[17:0] are used to MPU parallel interface data bus.	VDD3/ GND																																																																														
33	RD	I	This pin is used to “Read Clock” in 80-series parallel interface. This pin is used to “Read / Write Clock” in 68-series parallel interface.	VDD3/ GND																																																																														
34	WR	I	This pin is used to “Write Clock” in 80-series parallel interface. This pin is used to select “Read / Write Operation” in 68-series parallel interface. This pin is used serial interface clock in 4-wire 8-bit serial data interface.	VDD3/ GND																																																																														
35	DC/SCK	I/O	When DC/SCK = ‘1’, data is selected. When DC/SCK = ‘0’, command is selected.	VDD3/ GND																																																																														

36	SDO	O	Serial output signal. The data is outputted on the falling edge of the SCL signal.	OPEN
37	SDI	I	When IM[3] : Low, Serial in/out signal. When IM[3] : High, Serial input signal. The data is applied on the rising edge of the SCL signal.	VDD3/ GND
38	VDD3	I	Low voltage power supply for interface logic circuits (1.65 ~ 3.3 V)	-
39	CS	I	Chip select input pin ("Low" enable). This pin can be permanently fixed "Low" in MPU interface mode only.	-
40	GND	I	POWER GROUND	-

■ REFERENCE APPLICATION CIRCUIT

Please consult our technical department for detail information.

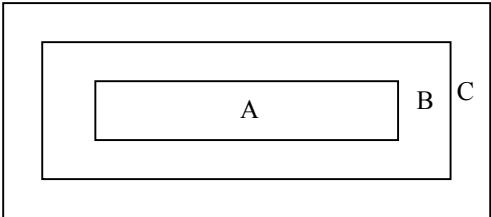
■ RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80 ± 2 °C / 200 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1. Air bubble in the LCD; 2. Sealleak; 3. Non-display; 4. missing segments; 5. Glass crack; 6. Current Idd is twice higher than initial value.
2	Low Temperature Storage	-30 ± 2 °C / 200 hours	
3	High Temperature Operating	70 ± 2 °C / 120 hours	
4	Low Temperature Operating	-20 ± 2 °C / 120 hours	
5	Temperature Cycle	-20 ± 2 °C ~ 25 ~ 70 ± 2 °C × 10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test	50 °C ± 5 °C × 90%RH / 120 hours	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours(Packing condition)	
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	
9	ESD test	Voltage: ±8KV R: 330Ω C: 150pF Air discharge, 10time	

Remark:

1. The test samples should be applied to only one test item.
2. Sample size for each test item is 5~10pcs.
3. For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
5. EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
6. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

■ INSPECTION CRITERION

TRULY [®] OUTGOING QUALITY STANDARD	PAGE 1 OF 4
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	MDS Product
<p>This specification is made to be used as the standard acceptance/rejection criteria for Color mobile phone LCM.</p> <p>1 Sample plan</p> <p>Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:</p> <p>Major defect: AQL 0.65</p> <p>Minor defect: AQL 1.5</p> <p>2. Inspection condition</p> <p>Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45°against perpendicular line.</p> <p>3. Definition of inspection zone in LCD.</p> <div style="text-align: center;"><p>The diagram shows three nested rectangles representing inspection zones. The innermost rectangle is labeled 'A'. The middle rectangle is labeled 'B' on its right side. The outermost rectangle is labeled 'C' on its right side.</p></div> <p>Zone A: character/Digit area</p> <p>Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)</p> <p>Zone C: Outside viewing area (invisible area after assembly in customer's product)</p> <p>Fig.1 Inspection zones in an LCD.</p> <p>Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.</p>	

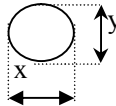
TRULY [®] OUTGOING QUALITY STANDARD	PAGE 2 OF 4
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	MDS Product

4. Inspection standards

4.1 Major Defect

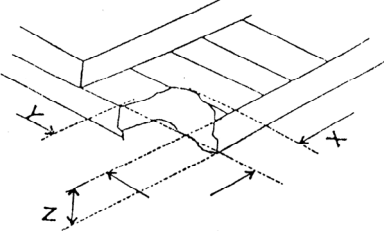
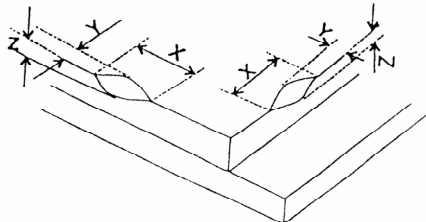
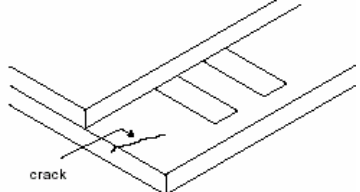
Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting.	Major
4.1.2	Missing	Missing component	
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	

4.2 Cosmetic Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects																											
4.2.1	Clear Spots	For dark/white spot, size Φ is defined as $\Phi = \frac{(x+y)}{2}$ 	Minor																											
	Black and white Spot defect Pinhole, Foreign Particle, Dirt under polarizer	1. <table border="1"> <thead> <tr> <th rowspan="2">Zone Size(mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td colspan="3">Ignore</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.15$</td> <td colspan="3">2</td> </tr> <tr> <td>$0.15 < \Phi \leq 0.20$</td> <td colspan="3">1</td> </tr> <tr> <td>$\Phi > 0.20$</td> <td colspan="3">0</td> </tr> </tbody> </table>		Zone Size(mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.10$	Ignore			$0.10 < \Phi \leq 0.15$	2			$0.15 < \Phi \leq 0.20$	1			$\Phi > 0.20$	0						
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$0.80 < \Phi$	0																													

4.2. Cosmetic Defect

Item No	Items to be inspected	Inspection Standard				Classification of defects	
4.2.2	Line defect Black line, White line, Foreign material under polarizer,	Size(mm)		Acceptable Qty		Minor	
		L(Length)	W(Width)	Zone			
				A	B		C
		Ignore	$W \leq 0.02$	Ignore			Ignore
		$L \leq 3.0$	$0.02 < W \leq 0.03$	2			
		$L \leq 2.0$	$0.03 < W \leq 0.05$	1			
	$0.05 < W$	Define as spot defect					
4.2.3	Polarizer scratch	<p>If the Polarizer scratch can be seen after mobile phone cover assembling or in the operating condition, judge by the line defect of 4.2.2.</p> <p>If the Polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following.</p>				Minor	
		Size(mm)		Acceptable Qty			
		L(Length)	W(Width)	Zone			
				A	B		C
		Ignore	$W \leq 0.03$	Ignore			Ignore
		$5.0 < L \leq 10.0$	$0.03 < W \leq 0.05$	2			
$L \leq 5.0$	$0.05 < W \leq 0.08$	1					
	$0.08 < W$	0					
4.2.4	Polarize Air bubble	Air bubbles between glass & polarizer				Minor	
		2. Zone Size(mm)	Acceptable Qty				
			A	B	C		
		$\Phi \leq 0.2$	Ignore		Ignore		
		$0.20 < \Phi \leq 0.30$	2				
$0.30 < \Phi \leq 0.50$	1						
$0.50 < \Phi$	0						

TRULY® OUTGOING QUALITY STANDARD		PAGE 4 OF 4							
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA		MDS Product							
4.3. Cosmetic Defect									
Item No	Items to be inspected	Inspection Standard	Classification of defects						
4.3.5	Glass defect	(i) Chips on corner  <table border="1" data-bbox="516 703 1112 793"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤2.0</td> <td>≤S</td> <td>Disregard</td> </tr> </table> <p>Notes: S=contact pad length Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.</p>	X	Y	Z	≤2.0	≤S	Disregard	Minor
		X	Y	Z					
		≤2.0	≤S	Disregard					
(ii) Usual surface cracks  <table border="1" data-bbox="495 1186 1128 1276"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤3.0</td> <td><Inner border line of the seal</td> <td>Disregard</td> </tr> </table>	X	Y	Z	≤3.0	<Inner border line of the seal	Disregard	Minor		
X	Y	Z							
≤3.0	<Inner border line of the seal	Disregard							
(iii) Crack Cracks tend to break are not allowed. 	Major								
4.3.6	Parts alignment	1) Not allow IC and FPC/heat-seal lead width is more than 50% beyond lead pattern. 2) Not allow chip or solder component is off center more than 50% of the pad outline.	Minor						
4.3.7	SMT	According to the <Acceptability of electronic assemblies> IPC-A-610C class 2 standard. Component missing or function defect are Major defect, the others are Minor defect.							

■ PRECAUTIONS FOR USING LCD MODULES

1 Handling Precautions

- 1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcoholDo not scrub hard to avoid damaging the display surface.
- 1.6 Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solventsWipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.
- 1.7 Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- 1.8 Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 1.9 Do not attempt to disassemble or process the LCD module.
- 1.10 NC terminal should be open. Do not connect anything.
- 1.11 If the logic circuit power is off, do not apply the input signals.
- 1.12 Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
 - To reduce the amount of static electricity generated, do not conduct assembling

and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

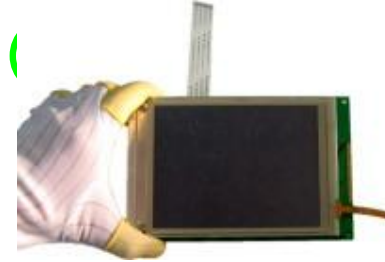
1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist the LCM.

2 Handling precaution for LCM

2.1 LCM is easy to be damaged. Please note below and be careful for handling.

2.2 Correct handling:

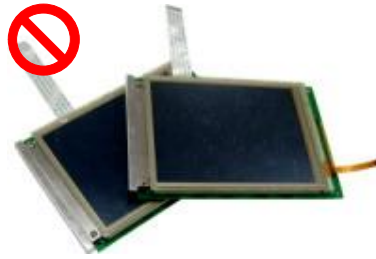


As above picture, please handle with anti-static gloves around LCM edges.

2.3 Incorrect handling:



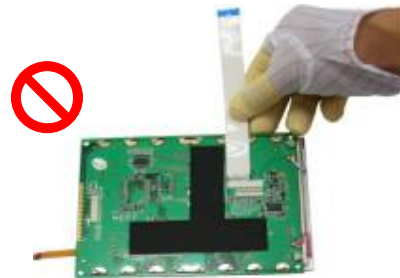
Please don't touch IC directly.



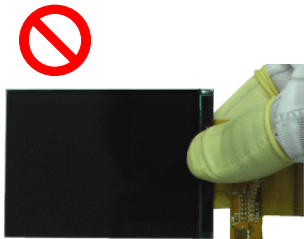
Please don't stack LCM.



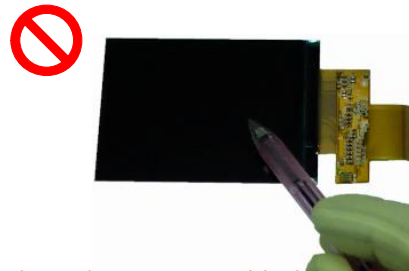
Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



Please don't hold the surface of IC.



Please don't operate with sharp stick such as pens.

3 Storage Precautions

3.1 When storing the LCD modules, the following precaution are necessary.

- 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).

3.2 Others 其它

- 3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- 3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
 - 3.2.3.1 - Exposed area of the printed circuit board.
 - 3.2.3.2 - Terminal electrode sections.

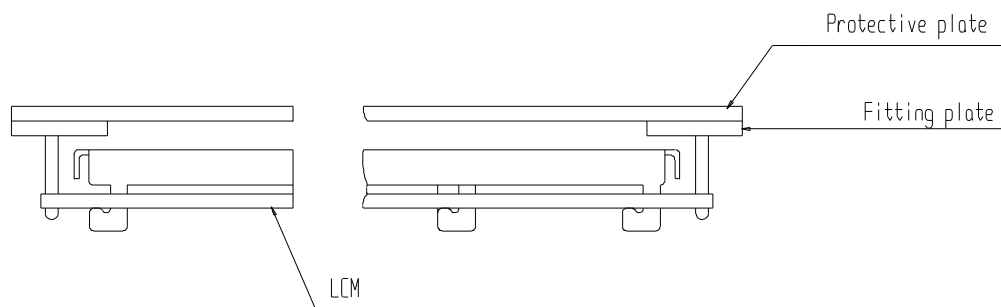
4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below.

Attend to the following items when installing the LCM.

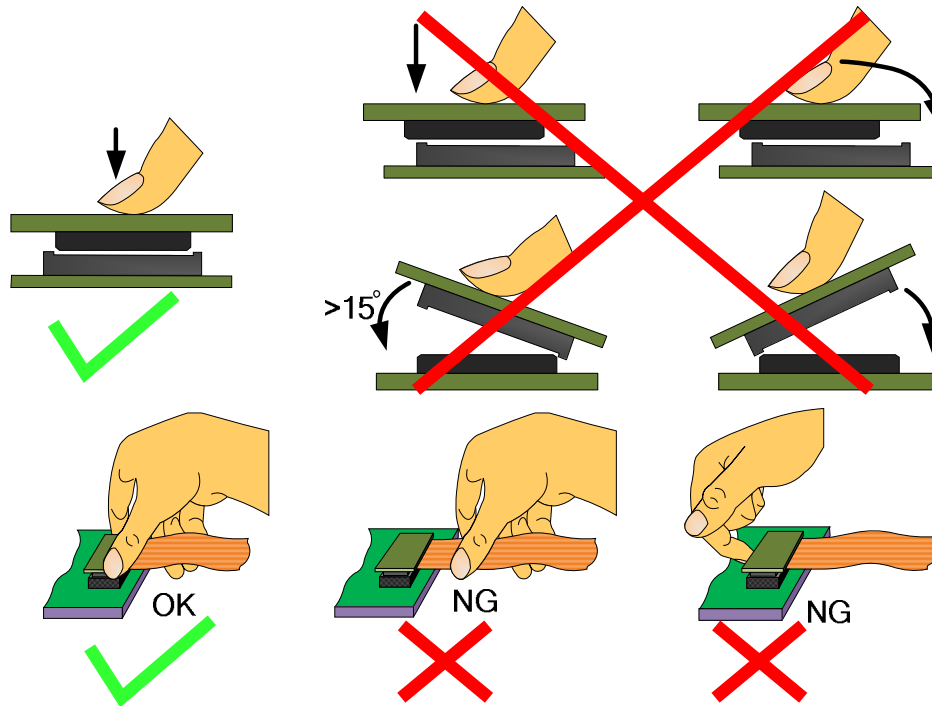
4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

4.2 Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



4.3 Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time : 3-5S.	330°C ~350°C. Speed : 15-17 mm/s.	300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time : 3-5S.	350°C ~370°C. Speed : 15-17 mm/s.	330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa

- 4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

4.4 Precautions for Operation

- 4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- 4.4.2 It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- 4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
- 4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- 4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- 4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- 4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

4.5 Safety

- 4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

4.6 Limited Warranty

Unless agreed between TRULY and the customer, TRULY will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with TRULY LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to TRULY within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of TRULY limited to repair and/or replace on the terms set forth above. TRULY will not be responsible for any subsequent or consequential events.

4.7 Return LCM under warranty

4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

4.7.1.1 - Broken LCD glass.

4.7.1.2 - PCB eyelet is damaged or modified.

4.7.1.3 -PCB conductors damaged.

4.7.1.4 - Circuit modified in any way, including addition of components.

4.7.1.5 - PCB tampered with by grinding, engraving or painting varnish.

4.7.1.6 - Soldering to or modifying the bezel in any manner.

4.7.2 Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PACKING SPECIFICATION

Please consult our technical department for detail information.

■ PRIOR CONSULT MATTER

- 1 For Truly standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
- 2 For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
- 3 If you have special requirement about reliability condition, please let us know before you start the test on our samples.

■ FACTORY CONTACT INFORMATION

FACTORY NAME: TRULY SEMICONDUCTORS LTD.

FACTORY ADDRESS: Truly Industrial Area, ShanWei City,GuangDong,China

P.C: 516600 **URL:** <http://www.truly.com.hk> <http://www.trulysemi.com>